

FIG. 1

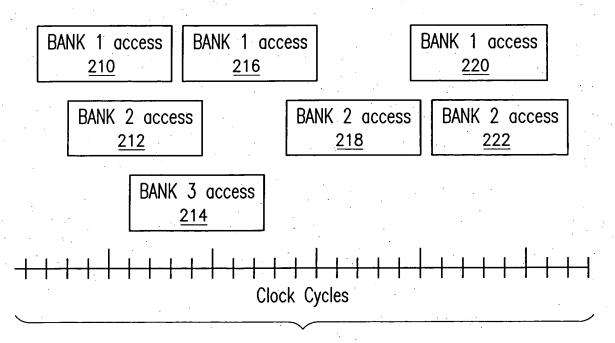


FIG. 2

Cmd	R	•	•	С	•	•	•	•	•	•	•	R	•	•	С	•	• ,	•	•	•	•	•	•
Data	•	•	•	•	•	•	D.	D	D	D	•	•	•	•	•	•	•	D	D	D	D	•	•

FIG. 3A

Cmd	R	•	•	С	•	•	•	•	•	<u>R</u>	•	R	<u>C</u>	<u>C</u>	Ç	•	•	•	•	•	•	•	•
Data	•	•	•	•	•	•	D	D	D	D	•	•	<u>D</u>	<u>D</u>	•	•	•	D	D	D	D	•	•

FIG. 3B

Cr	nd	R	•	•	С	•	•	•	•	•	•	•	R	•	•	C	•	•		•	•	•	•	•
Do	ata	•	•	•	•	•	•	D	D	D	D	•	•	•	•	•	•	•	D	<u>D</u>	D	D	•	•

FIG. 4A

Cmd	R	•	•	С	<u>R</u>	•	•	<u>C</u>	R	•	•	C	R	•	•	С	. •	•	•	•		•	•
Data	•	•	•	•	•	•	D	D.	D	D	<u>D</u>	<u>D</u>	<u>D</u>	D	D	D	D	D	D	D	D	D	•

FIG. 4B

Highest Address	0x7ffff —	
	0x5ffff —	BANK 4
	UXJIIII ——	BANK 3
Memory Address increases linearly	0x3ffff —	DANIZ O
	0x1ffff —	BANK 2
Lowest Address	0x0000 ——	BANK 1

FIG. 5

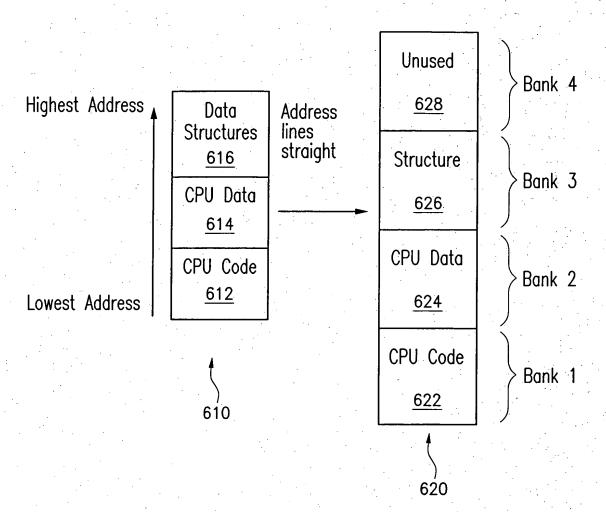


FIG. 6

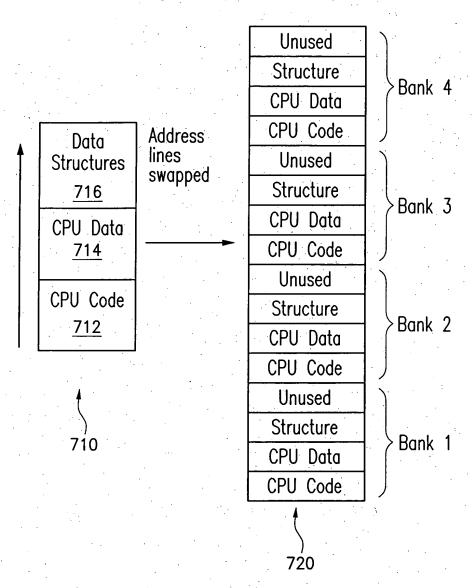


FIG. 7

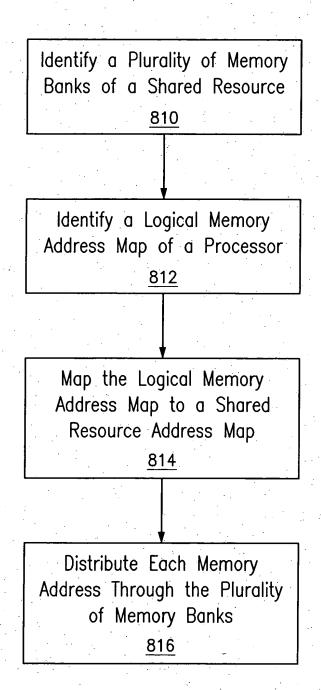


FIG. 8

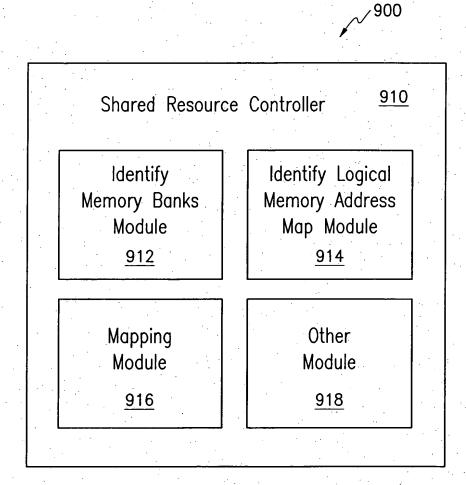


FIG. 9

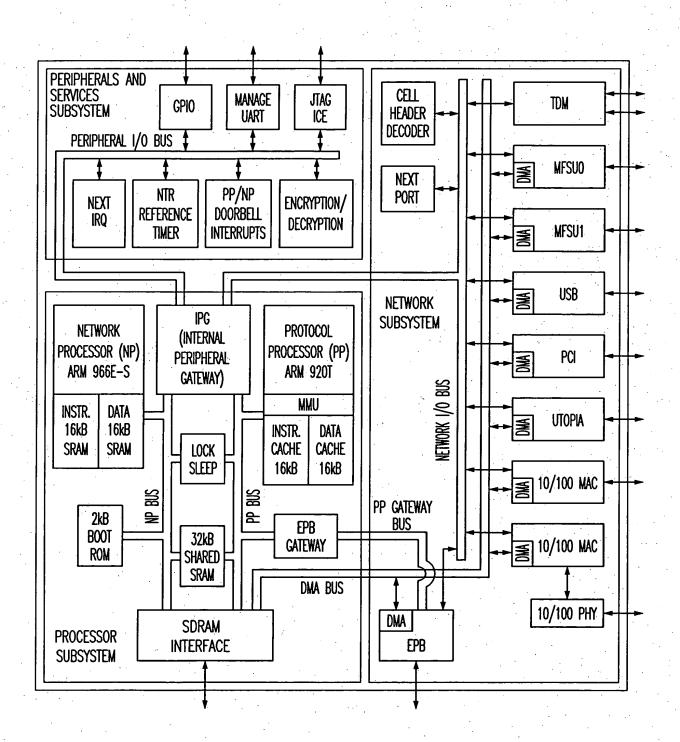


FIG. 10

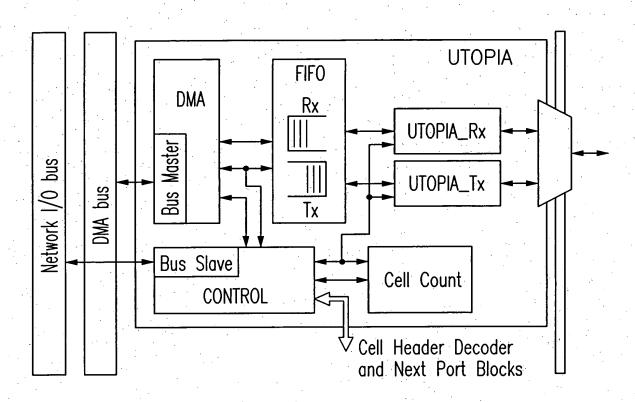
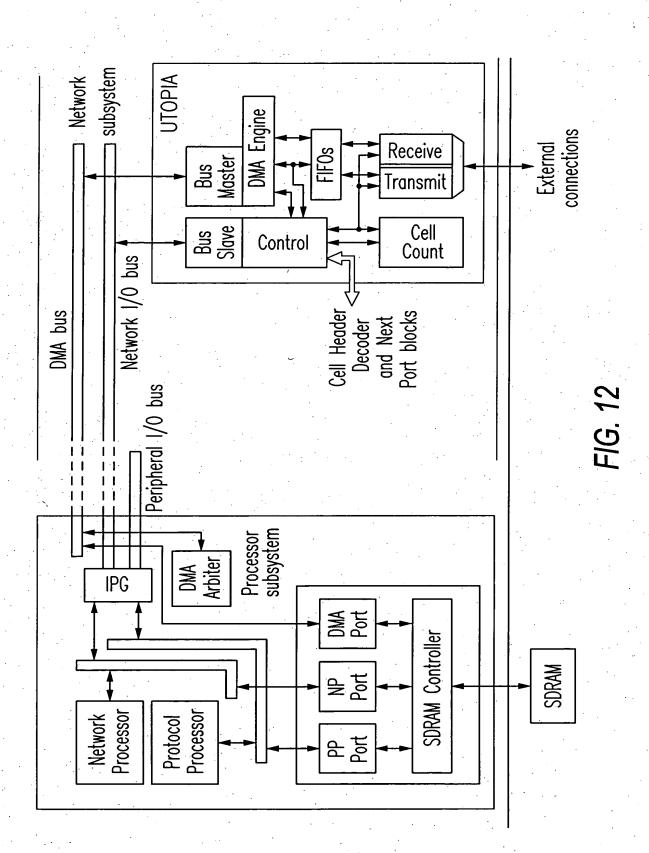


FIG. 11



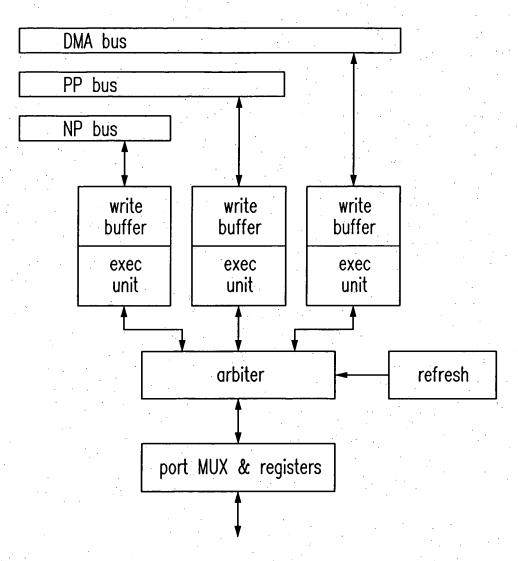
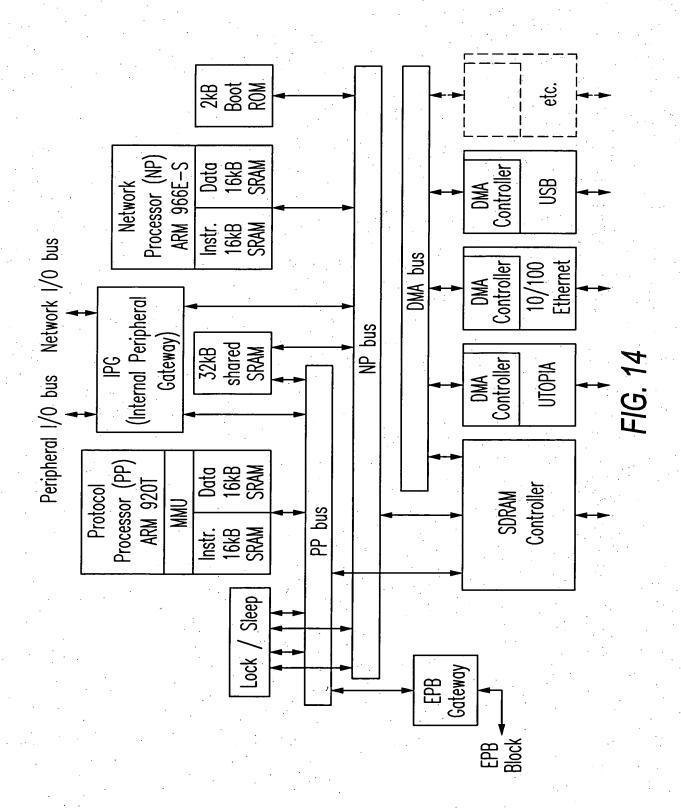


FIG. 13



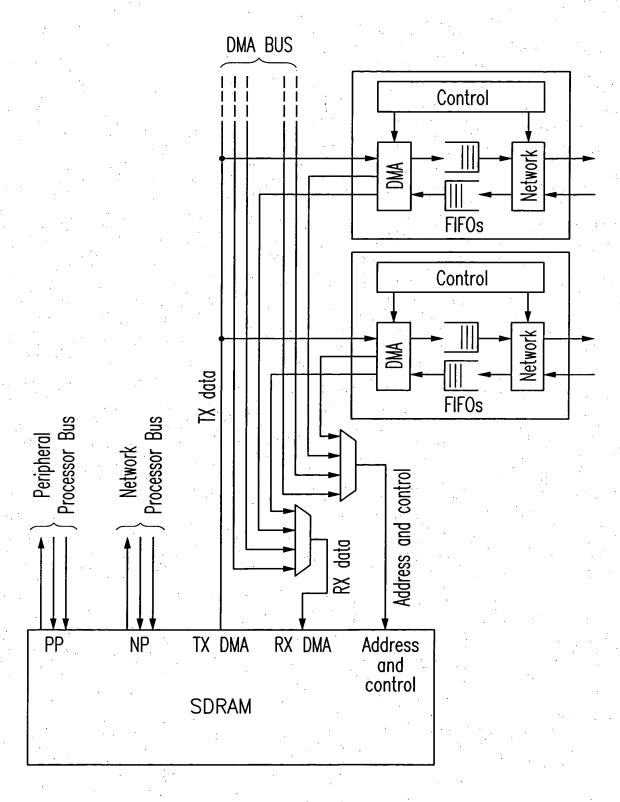


FIG. 15